* + 1. The name of the component

-NAND gate

* + 1. The inputs and their bit-size

-Inputs A and B of size 16

* + 1. The outputs and their bit-size

-Output C of size 16

* + 1. The interfaces and their bit-size

none

* + 1. Any controls and their bit-size. (Such as Multiplexor Select)

none

* + 1. A one-sentence description if necessary.

NAND gate that compares two inputs and outputs the ‘NAND” using truth table methods